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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Batch/Confirmation No.: 1207

Mutsumi KIMURA

Allowed: February 3, 2005

Group Art Unit: 2811

Application No.: 10/058,116

Examiner: G. Munson

Filed: January 29, 2002

Docket No.: 111829

For: SEMICONDUCTOR DEVICE, CIRCUIT BOARD, ELECTRO-OPTICAL  
DEVICE, AND ELECTRONIC APPARATUS

LETTER TO THE OFFICIAL DRAFTSPERSON

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please substitute the attached formal drawing depicting Figure 9 for the corresponding  
corrected Figure 9 filed on July 29, 2002.

Respectfully submitted,

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JAO:LMS/axl

Date: May 3, 2005

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